

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (Electronics & Communication)

VI - SEMESTER

Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme						Total Marks	Credits
				L	T	P	Theory			Practical				
							End Sem University Exam	Two Term Exam	Teachers Asses-ment*	End Sem University Exam	Teachers Asses-ment*			
1	BTEE503	Control System Engineering		3	1	2	60	20	20	30	20	150	5	
2	BTEC601	Wireless Communication		3	1	0	60	20	20	0	0	100	4	
3	BTEC602	Antenna and Microwave Engineering		3	1	2	60	20	20	30	20	150	5	
4	BTEC603	Discrete Time Signal Processing		3	1	2	60	20	20	30	20	150	5	
5	BTIT502/ BTEC614/ BTC5405/ BTEI614	Electives: 1 Computer Networks 2 Advance Digital Design(HDL) 3 Data Base Management System 4 Data Acquisition System		3	1	2	60	20	20	30	20	150	5	
6	BTEC606	Technical Communication & Soft Skills		1	0	0	0	0	50	0	0	50	1	
7	BTEC605	Personality Development -3		0	0	2	0	0	0	0	50	50	1	
TOTAL				16	5	10	300	100	150	120	130	800	26	

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

(Signature)
Chairperson

Board of Studies

**Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore**

(Signature)
Registrar

**Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore**